

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant :	Hideto Ohnuma et al.	Art Unit :	2812
Serial No. :	09/925,512	Examiner :	Richard A. Booth
Filed :	August 10, 2001	Confirmation No.:	4989
		Notice of Allowance Date:	January 24, 2007
Title :	METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE		

**MAIL STOP ISSUE FEE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO NOTICE OF ALLOWANCE**

In response to the Notice of Allowance mailed January 24, 2007, enclosed is a completed issue fee transmittal form PTOL-85b. The fees in the amount of \$1700 for the issue fee (\$1400) and the publication fee (\$300) are being paid concurrently herewith on the Electronic Filing System (EFS) by way of Deposit Account authorization. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: April 23, 2007

  
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